

# Model Numbering Guide – VCXO

## Available options

Type	package (mm)	Supply Voltage(V)	Tri-State Function	Freq.Stability/APR (ppm)	Temp. Range(°C)	Output Logic and Symmetry	Oscillator Mode	Appearance	Lead Free	Dash	Freq. (MHz)
V: VCXO	W: 5.0x3.2 T: 7.0x5.0 (6 Pads) K: 14.3x8.7	C: 5 E: 3.3 J:2.5 K: 1.8	U: Relative Pulling (Refer to Center Voltage) with Tri-State to pin 2 M: Multiplier Frequency with Tri-State to pin 2 S: Enable Low R: Input to pin 5 F: Without Tri-State A: Ultra Low Jitter design (Only for T package)	M: $\pm 25/\pm 50$ (VC=10%Vdd ~90%Vdd) P: $\pm 50/\pm 50$ (VC=10%Vdd ~90%Vdd) A: $\pm 50/\pm 50$ (VC=0V~Vdd) B: $\pm 25/\pm 50$ (VC=0V~Vdd)	I: -10~+60 C: -20~+70 L: -40~+85 J: -40~+105	J: CMOS 15pF / 50±5% F: CMOS 50pF / 50±5% L: LVPECL / 50±5% V: LVDS / 50±5%	A: AT Fundamental  Not selectable by Cxxustomer	N :Normal	F: RoHs Compliant	-	XX.XXXXXX

**V T E S P C L A N F – 10.000000**

\*Not all combinations of options are available.

### Example: VTESPCLANF-10.000000

Type	VCXO
Package	7.0 x 5.0 mm
Supply Voltage(V)	3.3 V
Tri-State	Enable Low
Freq. Stability / APR	$\pm 50$ ppm / $\pm 50$ ppm
Temp Range	-20~+70 °C
Output	LVPECL/Symmetry 50±5%
Oscillator Mode	AT Fundamental
Appearance	Normal Appearance
Lead Free	RoHs Compliant
Frequency	10.000000 MHz