



Fast transient response, rush current protection 200mA LDO

MM3763 Series

Overview

This IC is a 200mA Low dropout regulator IC with Rush current protection circuit. No load input current is 25μA typ. and it reduce drop voltage for high speed response. Rush current protection circuit can control rush current. The package is a small PLP-4C (1mm x 1mm), ideal for mobile devices.

Features

- Rush current protection
- High speed response
- Small package

Main specifications

($V_{DD}=V_{OUT}(Typ.)+1V$, $V_{CE}=V_{DD}$, $T_a=25^{\circ}C$ unless otherwise specified)

- Maximum rating supply voltage : -0.3V to 7V
- Operating voltage range : 1.8V to 6.5V
- Operating ambient temperature : -40°C to 85°C
- Output current : 200mA
- Input current (OFF) : Typ. 0.01uA
- No-load input current : Typ. 25uA
- Output voltage range : 0.8V to 5V (0.05V step)
- Output voltage accuracy : $\pm 1\%$ ($2.0V \leq V_{OUT}(Typ.)$)
 $\pm 20mV$ ($V_{OUT}(Typ.) < 2.0V$)
- Line regulation : Typ. 0.01%/V ($1.1V \leq V_{OUT}(Typ.)$, $V_{DD}=V_{OUT}(Typ.)+0.5V$ to 6.5V)
Typ. 0.01%/V ($V_{OUT}(Typ.) < 1.1V$, $V_{DD}=V_{OUT}(Typ.)+1V$ to 6.5V)
- Load regulation : Typ. 10mV ($I_{OUT}=1mA$ to 150mA)
Typ. 20mV ($I_{OUT}=1mA$ to 200mA)
- Dropout voltage : Typ. 0.4V ($I_{OUT}=200mA$, $V_{OUT}(Typ.)=3V$)
- PSRR : Typ. 70dB ($f=1kHz$)
- Output capacitor : 0.47uF (Ceramic capacitor)
- Protection function : Over current protection, Rush current protection
- Additional function : ON/OFF control, Auto discharge

Packages

- PLP-4C

Application

- Audio visual equipment
- Portable communication device
- Photographing / Imaging device
- Wearable device





Model Name

M M 3 7 6 3 X X X X X E

Series name

(A)

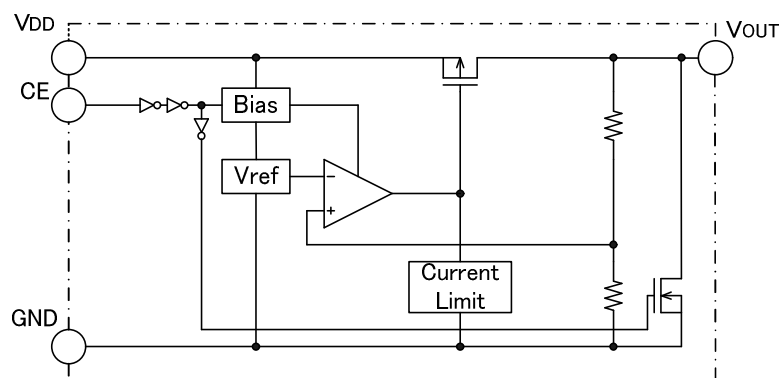
(B)

(C)

(D)

(A)	Function Type	A	CE=H active, with discharge function
		Z	
(B)	Output voltage rank	08	(A)="A" the output voltage can be designated in the range from 0.80V(08) to 5.00V(50) in 0.1V steps. (A)="Z" the output voltage can be designated in the range from 0.85V(08) to 4.95V(49) in 0.05V steps.
		2	
		50	
(C)	Package	R	PLP-4C
(D)	Packing specifications	R	R housing (Standard)

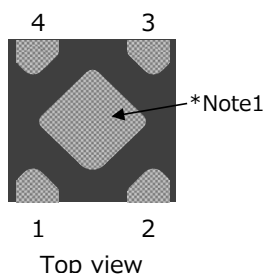
Block Diagram





Pin Configuration

■ PLP-4C



Pin No.	Pin name	Function
1	V_{OUT}	Output pin
2	GND	GND pin
3	CE	ON/OFF-control pin Connect CE pin with VDD pin, when it is not used.
4	V_{DD}	Voltage supply pin

*Note1: Heat spreader bottom with GND.





Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit
Storage temperature	T _{stg}	-55	150	°C
Junction temperature	T _{JMAX}	-	150	°C
Supply voltage	V _{DD}	-0.3	7.0	V
CE input voltage	V _{CE}	-0.3	7.0	V
Output voltage	V _{OUT}	-0.3	7.0	V
Output current	I _{OUT}	0	400	mA
Power dissipation *Note2	Pd1	-	1300	mW

*Note2: JEDEC51-7 standard

Recommended Operating Conditions

Item	Symbol	Min.	Max.	Unit
Operating ambient temperature	T _{opr}	-40	85	°C
Operating voltage	V _{op}	1.8	6.5	V
Output current	I _{op}	0	200	mA

Electrical Characteristics

(V_{DD}=V_{OUT}(Typ.)+1V, V_{CE}=V_{DD}, T_a=25°C unless otherwise specified)

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input current(OFF)	I _{DDOFF}	V _{CE} =0V	-	0.01	1.0	μA
No-Load Input Current	I _{DD}	I _{OUT} =0mA	-	25	40	μA
Output voltage	V _{OUT}	I _{OUT} =10mA 2.00V≤V _{OUT}	×0.99	-	×1.01	V
		I _{OUT} =10mA V _{OUT} <2.00V	-0.02	-	0.02	V
Line regulation	V _{LINE}	V _{OUT} (Typ.)+0.5V≤V _{DD} ≤6.5V	-	0.01	0.10	% / V
		V _{OUT} (Typ.)≤1.10V, I _{OUT} =10mA				
		V _{OUT} (Typ.)+1.0V≤V _{DD} ≤6.5V V _{OUT} (Typ.)<1.05V, I _{OUT} =10mA				
Load regulation 1	V _{LOAD1}	1mA≤I _{OUT} ≤150mA	-	10	40	mV
Load regulation 2	V _{LOAD2}	1mA≤I _{OUT} ≤200mA	-	20	60	mV
Dropout voltage	V _{io}	別紙参照	-	-	-	V
Ripple rejection *Note3	RR	f=1kHz, V _{ripple} =0.5V I _{OUT} =10mA	-	70	-	dB
Vout temperature coefficient *Note3	ΔV _{OUT} /ΔT	I _{OUT} =10mA -40≤T _{op} ≤85°C	-	±100	-	ppm/°C

*Note3: The parameter is guaranteed by design.





Electrical Characteristics

($V_{DD}=V_{OUT}(Typ.)+1V$, $V_{CE}=V_{DD}$, $T_a=25^{\circ}C$ unless otherwise specified)

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Output current limit	I_{lim}		200	350	-	mA
Output short-circuit current *Note3	I_{short}	$V_{OUT}=0V$	-	20	-	mA
CE High threshold voltage	V_{CEH}		1.5	-	6.5	V
CE Low threshold voltage	V_{CEL}		-	-	0.3	V
CE High threshold current	I_{CEH}		-0.1	-	0.1	μA
CE Low threshold current	I_{CEL}		-0.1	-	0.1	μA
CL discharge resistance *Note3	R_{dsc}	$V_{CE}=0V$, $V_{DD}=6V$	-	10	-	Ω

*Note3: The parameter is guaranteed by design.





Electrical Characteristics

($V_{DD}=V_{OUT}(Typ.)+1V$, $V_{CE}=V_{DD}$, $T_a=25^{\circ}C$ unless otherwise specified)

Model name	Item							
	Output voltage				Dropout voltage			
	V_{OUT} (V)				V_{IO} (V)			
	Conditions	Min.	Typ.	Max.	Conditions	Min.	Typ.	Max.
MM3763A08	$I_{OUT}=10mA$	0.780	0.800	0.820	$I_{OUT}=150mA$ $0.8V \leq V_{OUT}(Typ.) < 1.9V$ *Note4	-	0.77	0.88
MM3763A09		0.880	0.900	0.920				
MM3763A10		0.980	1.000	1.020				
MM3763A11		1.080	1.100	1.120		-	0.69	0.79
MM3763A12		1.180	1.200	1.220				
MM3763A13		1.280	1.300	1.320				
MM3763A14		1.380	1.400	1.420		-	0.60	0.70
MM3763A15		1.480	1.500	1.520				
MM3763A16		1.580	1.600	1.620				
MM3763A17		1.680	1.700	1.720		-	0.51	0.61
MM3763A18		1.780	1.800	1.820				
MM3763A19		1.880	1.900	1.920	$I_{OUT}=150mA$ $1.9V \leq V_{OUT} \leq 5.0V$ $V_{DD}=V_{OUT}(TYP.)-0.2V$	-	0.47	0.57
MM3763A20		1.980	2.000	2.020				
MM3763A21		2.079	2.100	2.121				
MM3763A22		2.178	2.200	2.222				
MM3763A23		2.277	2.300	2.323				
MM3763A24		2.376	2.400	2.424				
MM3763A25		2.475	2.500	2.525				
MM3763A26		2.574	2.600	2.626				
MM3763A27		2.673	2.700	2.727				
MM3763A28		2.772	2.800	2.828				
MM3763A29		2.871	2.900	2.929		-	0.31	0.41
MM3763A30		2.970	3.000	3.030				
MM3763A31		3.069	3.100	3.131				
MM3763A32		3.168	3.200	3.232				
MM3763A33		3.267	3.300	3.333				
MM3763A34		3.366	3.400	3.434				
MM3763A35		3.465	3.500	3.535				
MM3763A36		3.564	3.600	3.636				
MM3763A37		3.663	3.700	3.737				
MM3763A38		3.762	3.800	3.838		-	0.23	0.33
MM3763A39		3.861	3.900	3.939				
MM3763A40		3.960	4.000	4.040				
MM3763A41		4.059	4.100	4.141				
MM3763A42		4.158	4.200	4.242				
MM3763A43		4.257	4.300	4.343				
MM3763A44		4.356	4.400	4.444				
MM3763A45		4.455	4.500	4.545				
MM3763A46		4.554	4.600	4.646				
MM3763A47		4.653	4.700	4.747		-	0.19	0.28
MM3763A48		4.752	4.800	4.848				
MM3763A49		4.851	4.900	4.949				
MM3763A50		4.950	5.000	5.050				

*Note4: Dropout voltage MAX value in the input and it is confirmed that there is no output abnormal voltage impression the load 150mA in the model less than $V_{out} < 1.9V$.





Electrical Characteristics

($V_{DD}=V_{OUT}(Typ.)+1V$, $V_{CE}=V_{DD}$, $T_a=25^{\circ}C$ unless otherwise specified)

Model name	Item							
	Output voltage				Dropout voltage			
	V_{OUT} (V)				V_{IO} (V)			
	Conditions	Min.	Typ.	Max.	Conditions	Min.	Typ.	Max.
MM3763Z08	$I_{OUT}=10mA$	0.830	0.850	0.870	$I_{OUT}=150mA$ $0.8V \leq V_{OUT}(Typ.) < 1.9V$ *Note4	-	0.77	0.88
MM3763Z09		0.930	0.950	0.970				
MM3763Z10		1.030	1.050	1.070				
MM3763Z11		1.130	1.150	1.170		-	0.69	0.79
MM3763Z12		1.230	1.250	1.270				
MM3763Z13		1.330	1.350	1.370				
MM3763Z14		1.430	1.450	1.470		-	0.60	0.70
MM3763Z15		1.530	1.550	1.570				
MM3763Z16		1.630	1.650	1.670				
MM3763Z17		1.730	1.750	1.770		-	0.51	0.61
MM3763Z18		1.830	1.850	1.870				
MM3763Z19		1.930	1.950	1.970	$I_{OUT}=150mA$ $1.9V \leq V_{OUT} \leq 5.0V$ $V_{DD}=V_{OUT}(TYP.)-0.2V$	-	0.47	0.57
MM3763Z20		2.030	2.050	2.071				
MM3763Z21		2.129	2.150	2.172				
MM3763Z22		2.228	2.250	2.273				
MM3763Z23		2.327	2.350	2.374				
MM3763Z24		2.426	2.450	2.475				
MM3763Z25		2.525	2.550	2.576				
MM3763Z26		2.624	2.650	2.677				
MM3763Z27		2.723	2.750	2.778				
MM3763Z28		2.822	2.850	2.879				
MM3763Z29		2.921	2.950	2.980		-	0.31	0.41
MM3763Z30		3.020	3.050	3.081				
MM3763Z31		3.119	3.150	3.182				
MM3763Z32		3.218	3.250	3.283				
MM3763Z33		3.317	3.350	3.384				
MM3763Z34		3.416	3.450	3.485				
MM3763Z35		3.515	3.550	3.586				
MM3763Z36		3.614	3.650	3.687				
MM3763Z37		3.713	3.750	3.788				
MM3763Z38		3.812	3.850	3.889				
MM3763Z39		3.911	3.950	3.990		-	0.23	0.33
MM3763Z40		4.010	4.050	4.091				
MM3763Z41		4.109	4.150	4.192				
MM3763Z42		4.208	4.250	4.293				
MM3763Z43		4.307	4.350	4.394				
MM3763Z44		4.406	4.450	4.495				
MM3763Z45		4.505	4.550	4.596				
MM3763Z46		4.604	4.650	4.697				
MM3763Z47		4.703	4.750	4.798		-	0.19	0.28
MM3763Z48		4.802	4.850	4.899				
MM3763Z49		4.901	4.950	5.000				

*Note4: Dropout voltage MAX value in the input and it is confirmed that there is no output abnormal voltage impression the load 150mA in the model less than $V_{out} < 1.9V$.





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	Conditions	Min.	Typ.	Max.	Conditions	Min.	Typ.	Max.
MM3763A08	$I_{OUT}=10mA$	0.780	0.800	0.820	$I_{OUT}=200mA$ $0.8V \leq V_{OUT}(Typ.) < 1.9V$ *Note5	-	1.00	1.15
MM3763A09		0.880	0.900	0.920				
MM3763A10		0.980	1.000	1.020				
MM3763A11		1.080	1.100	1.120		-	0.90	1.04
MM3763A12		1.180	1.200	1.220				
MM3763A13		1.280	1.300	1.320				
MM3763A14		1.380	1.400	1.420		-	0.78	0.90
MM3763A15		1.480	1.500	1.520				
MM3763A16		1.580	1.600	1.620				
MM3763A17		1.680	1.700	1.720		-	0.67	0.77
MM3763A18		1.780	1.800	1.820				
MM3763A19		1.880	1.900	1.920	$I_{OUT}=200mA$ $1.9V \leq V_{OUT} \leq 5.0V$ $V_{DD}=V_{OUT}(TYP.)-0.2V$	-	0.62	0.72
MM3763A20		1.980	2.000	2.020				
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MM3763A49		4.851	4.900	4.949				
MM3763A50		4.950	5.000	5.050				

*Note5: Dropout voltage MAX value in the input and it is confirmed that there is no output abnormal voltage impression the load 200mA in the model less than $V_{out} < 1.9V$.





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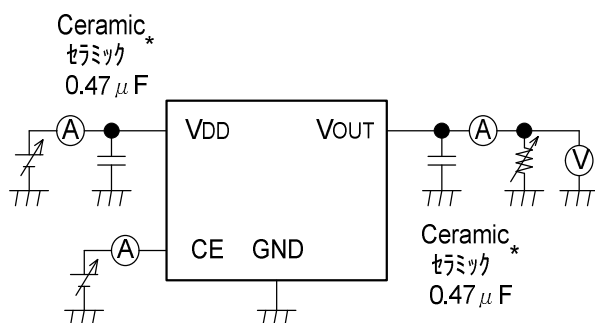
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MM3763Z12		1.230	1.250	1.270		-	0.62	0.72
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MM3763Z23		2.327	2.350	2.374		-	0.62	0.72
MM3763Z24		2.426	2.450	2.475		-	0.62	0.72
MM3763Z25		2.525	2.550	2.576		-	0.62	0.72
MM3763Z26		2.624	2.650	2.677		-	0.62	0.72
MM3763Z27		2.723	2.750	2.778		-	0.62	0.72
MM3763Z28		2.822	2.850	2.879		-	0.62	0.72
MM3763Z29		2.921	2.950	2.980		-	0.62	0.72
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MM3763Z34		3.416	3.450	3.485		-	0.62	0.72
MM3763Z35		3.515	3.550	3.586		-	0.62	0.72
MM3763Z36		3.614	3.650	3.687		-	0.62	0.72
MM3763Z37		3.713	3.750	3.788		-	0.62	0.72
MM3763Z38		3.812	3.850	3.889		-	0.62	0.72
MM3763Z39		3.911	3.950	3.990		-	0.62	0.72
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MM3763Z46		4.604	4.650	4.697		-	0.62	0.72
MM3763Z47		4.703	4.750	4.798		-	0.62	0.72
MM3763Z48		4.802	4.850	4.899		-	0.62	0.72
MM3763Z49		4.901	4.950	5.000		-	0.62	0.72

*Note5: Dropout voltage MAX value in the input and it is confirmed that there is no output abnormal voltage impression the load 200mA in the model less than $V_{out} < 1.9V$.

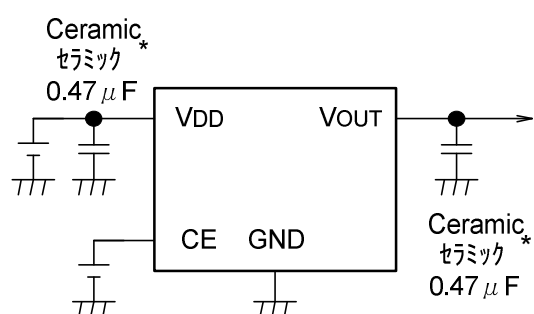




Test Circuit



Application Circuit



(Example of external parts)

- Output capacitor Ceramic capacitor 0.47 μ F
- Input capacitor Ceramic capacitor 0.47 μ F

*Temperature characteristics : B

- In the event a problem which may affect industrial property or any other rights of us or a third party is encountered during the use of information described in these circuit, we shall not be liable for any such problem, nor grant a license therefore.





Note

1. Please use this IC within the stated absolute maximum ratings.
The IC is liable to malfunction should the ratings be exceeded.
2. Due to restrictions on the package power dissipation, the output current value may not be satisfied.
Attention should be paid to the power dissipation of the package when the output current is large or the voltage between Iinput and Output is high.
3. The output capacitor is required between output and GND to prevent oscillation.
4. The ESR of capacitor must be defined in ESR stability area.
It is possible to use a ceramic capacitor without ESR resistance for output.
The ceramic capacitor must be used more than 0.1 μ F and B temperature characteristics.
5. The wire of VDD and GND is required to print full ground plane for noise and stability.
6. The input capacitor must be connected a distance of less than 1cm from input pin.
7. It is able to oscillation when you use the capacitor with intense capacitance change such as micro.
Please evaluate IC in the set.
8. In case the output voltage is above the input voltage, the overcurrent flow by internal parastic diode from output to input.
In such application, the external bypass diode must be connected between output and input pin.
9. This IC will limit the output current with the overcurrent protection circuit when the overcurrent and the output do short-circuit.
However, IC generates heat because of the substrate and use conditions and there is a possibility of destroying it exceeding a permissible loss.
The characteristic changes depending on the substrate condition.
Please evaluate IC in the set.
10. In case the output capacitor is over 2.2 μ F and steady current is under 5mA, it is able to oscillate.
It is recommended that the output capacitor is under 2.2 μ F on condition that the current is under 5mA.
Please evaluate IC in the set if it is used in the above condition .

Complement : The oscillation is low level noise (200~300uVrms/Vout=3.0V).

So the above condition is recommended only if it is used for a sensitive sensor against noise .
Except for sensitive part against noise, the restriction of above condition is not required.





About Power Dissipation

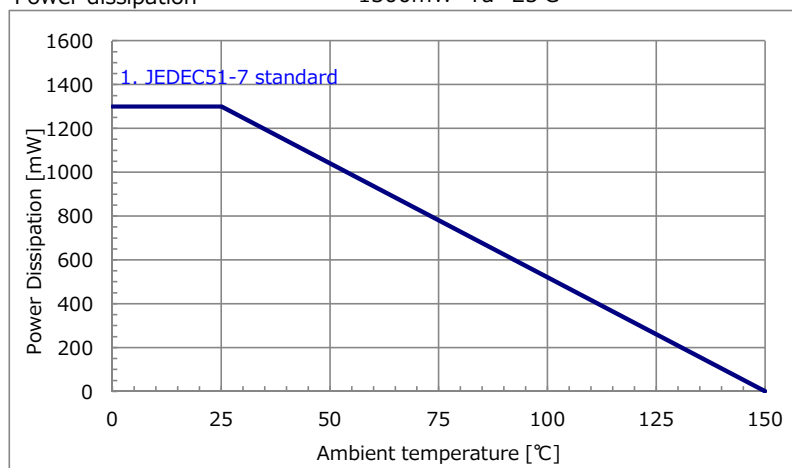
The Power dissipation change if board to mount IC change because radiative heat fix at board.
It is reference data below, Evaluate IC in the set.

■ PLP-4C

1. JEDEC51-7 standard (4 layer FR-4 board)

Board size 114.3mm×76.2mm t=1.6mm Copper foil area 80%

Power dissipation 1300mW Ta=25°C



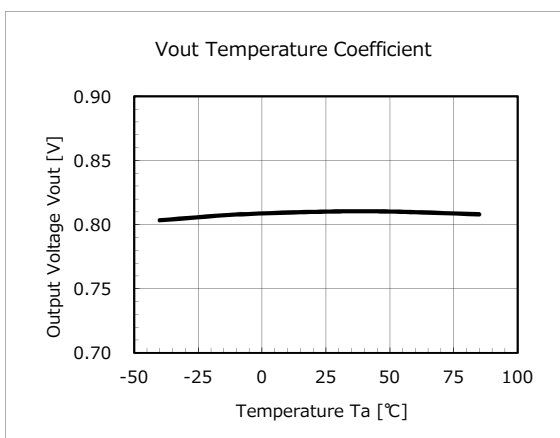
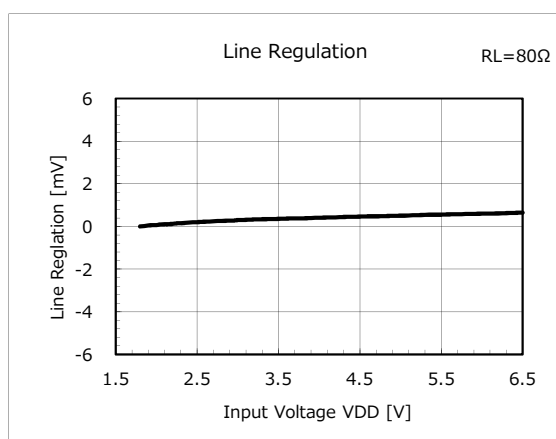
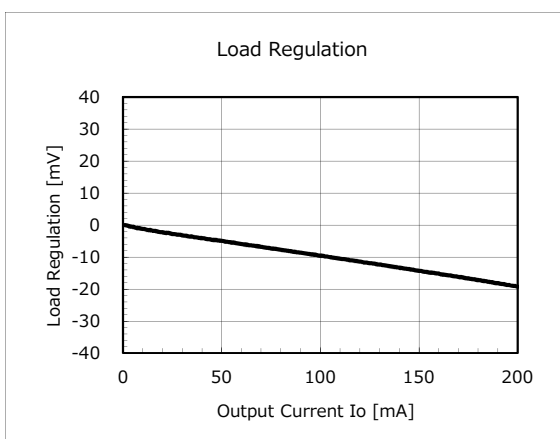
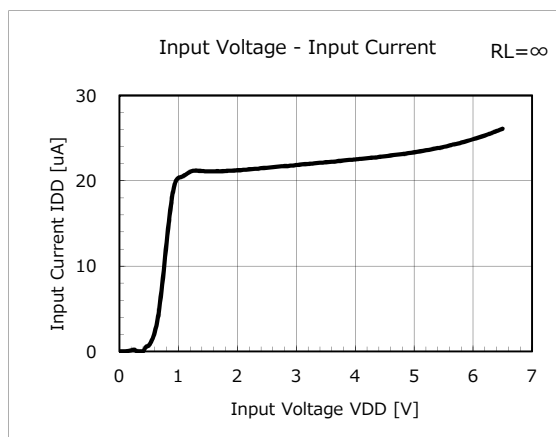
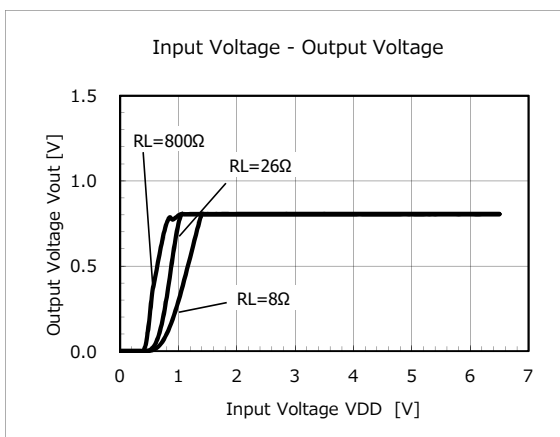
It is recommended to layout the VIA for heat radiation in the GND pattern of reverse (of IC) when there is the GND pattern in the inner layer (in using multiplayer substrate).
By increasing these copper foil pattern area of PCB, Power dissipation improves.





Typical Performance Characteristics (0.8V)

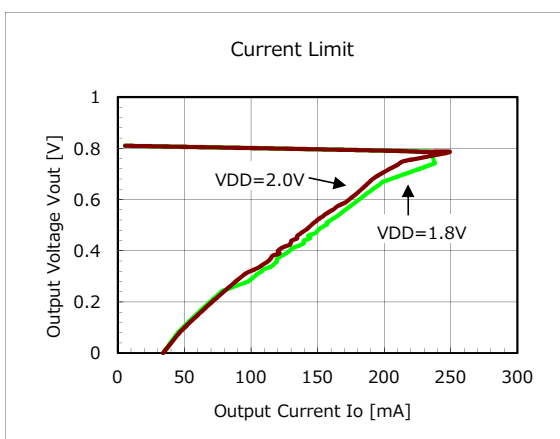
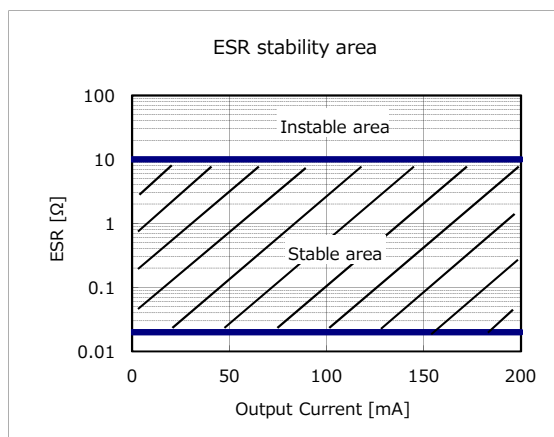
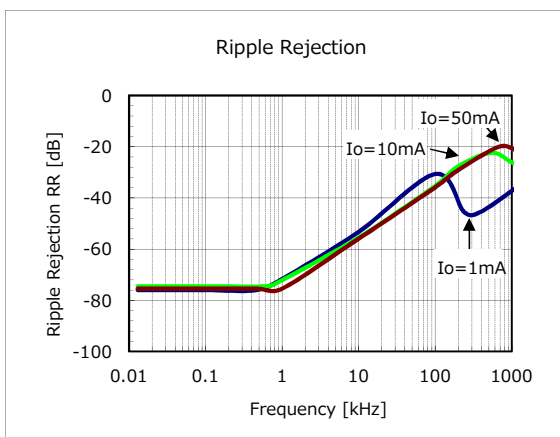
($V_{DD}=V_{OUT(Typ.)}+1V$, $V_{CE}=V_{DD}$, $T_a=25^{\circ}C$ unless otherwise specified)





Typical Performance Characteristics (0.8V)

($V_{DD}=V_{OUT(Typ.)}+1V$, $V_{CE}=V_{DD}$, $T_a=25^{\circ}C$ unless otherwise specified)





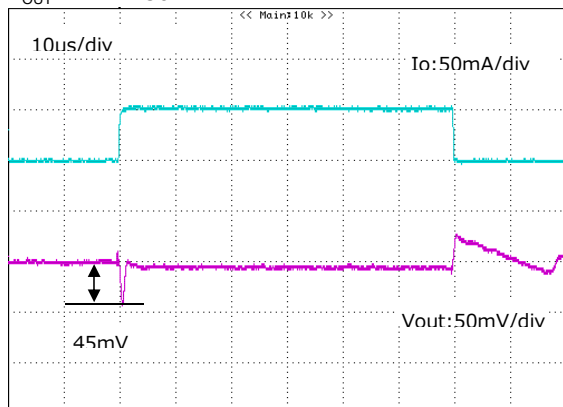
Typical Performance Characteristics (1.0V)

$(V_{DD}=V_{OUT(Typ.)}+1V, V_{CE}=V_{DD}, T_a=25^{\circ}C \text{ unless otherwise specified})$

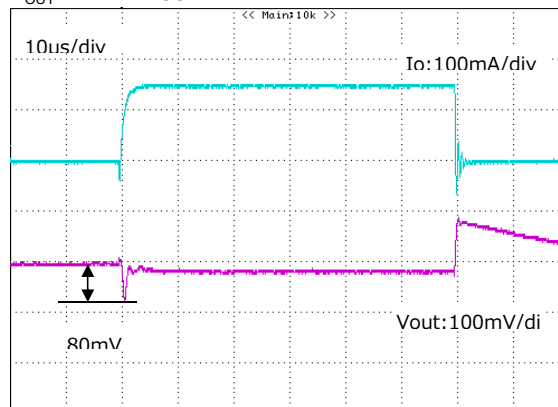
Load transient response

$(V_{DD}=V_{OUT}+1V, V_{CE}=V_{DD}, C_{in}=C_o=0.47\mu F)$

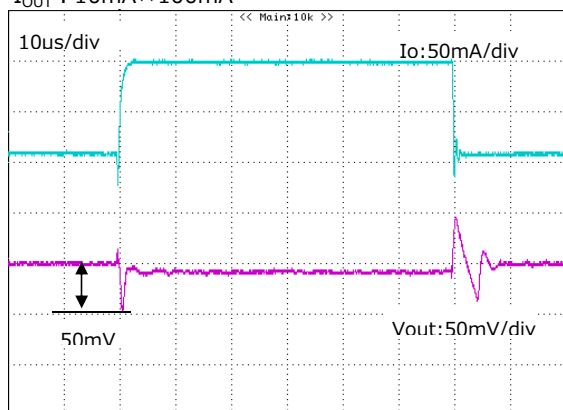
$I_{OUT} : 1mA \leftrightarrow 50mA$



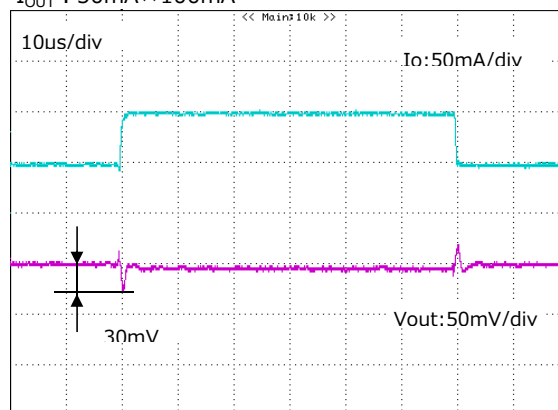
$I_{OUT} : 1mA \leftrightarrow 150mA$



$I_{OUT} : 10mA \leftrightarrow 100mA$

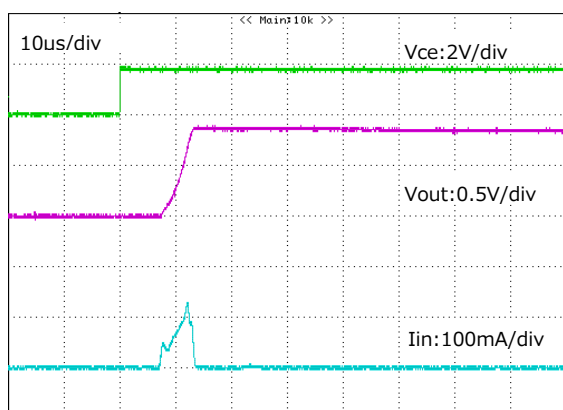


$I_{OUT} : 50mA \leftrightarrow 100mA$

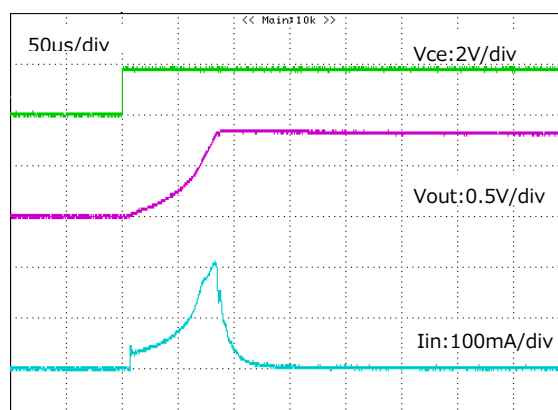


CE transient

$(V_{DD}=1.8V, V_{CE}=0V \rightarrow V_{DD}, C_o=0.47\mu F)$



$(V_{DD}=1.8V, V_{CE}=0V \rightarrow V_{DD}, C_o=10\mu F)$



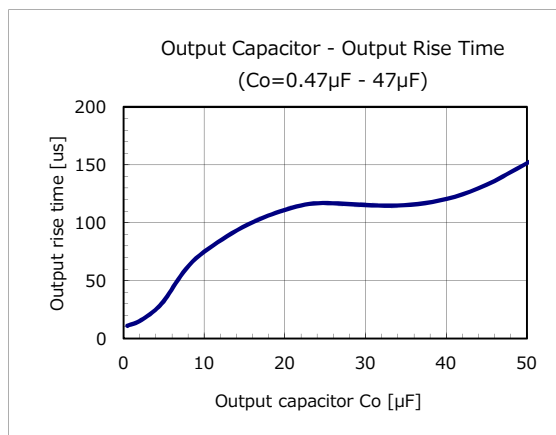
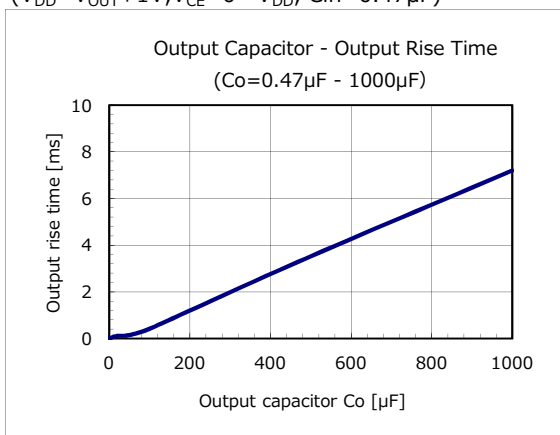


Typical Performance Characteristics (0.8V)

($V_{DD}=V_{OUT(Typ.)}+1V$, $V_{CE}=V_{DD}$, $T_a=25^{\circ}C$ unless otherwise specified)

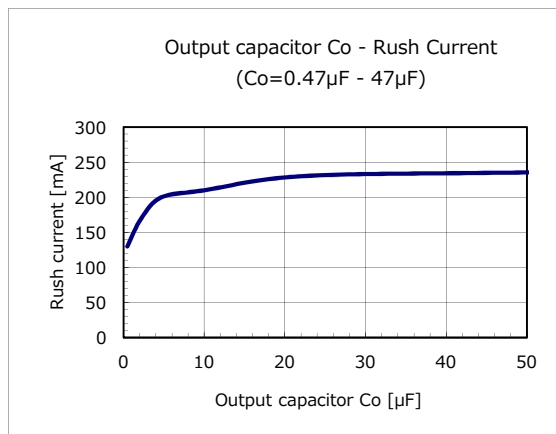
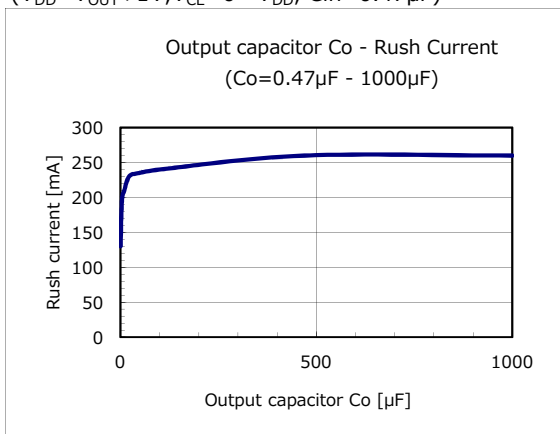
■ Output Rise Time

($V_{DD}=V_{OUT}+1V$, $V_{CE}=0 \rightarrow V_{DD}$, $C_{in}=0.47\mu F$)



■ Rush Current

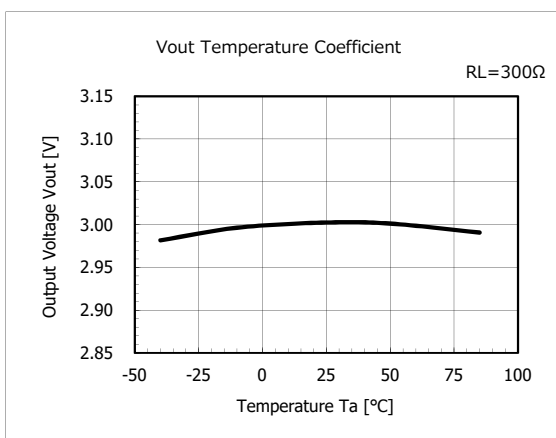
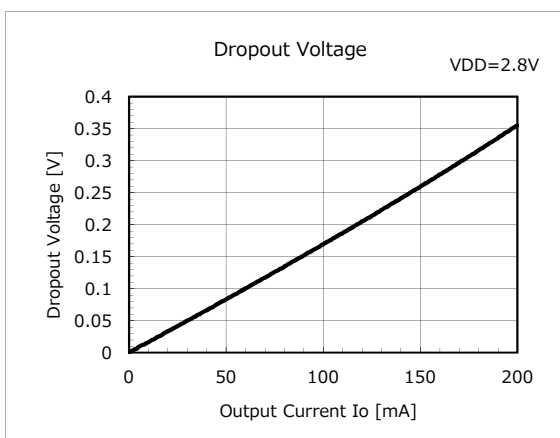
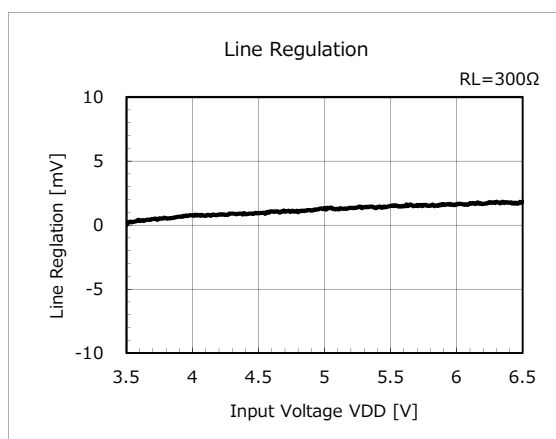
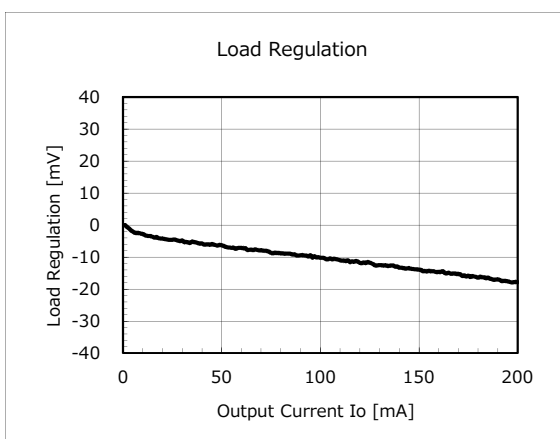
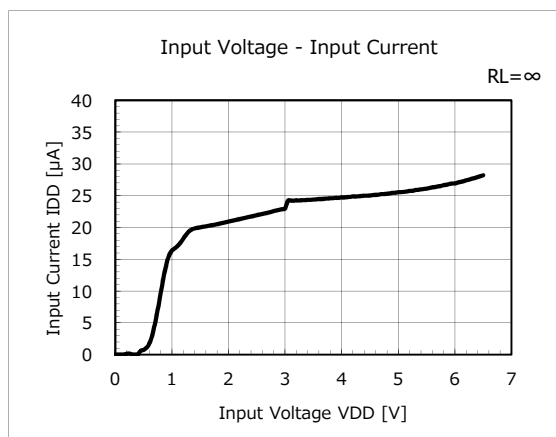
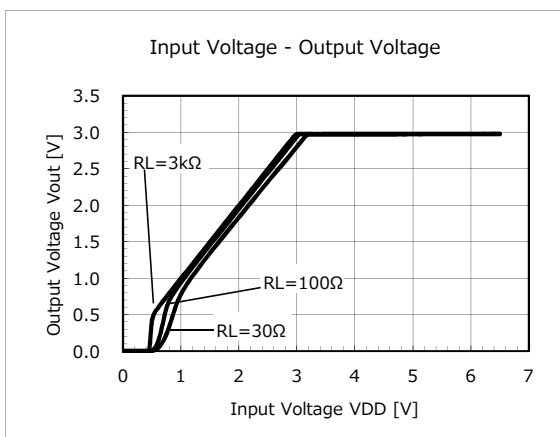
($V_{DD}=V_{OUT}+1V$, $V_{CE}=0 \rightarrow V_{DD}$, $C_{in}=0.47\mu F$)





Typical Performance Characteristics (3.0V)

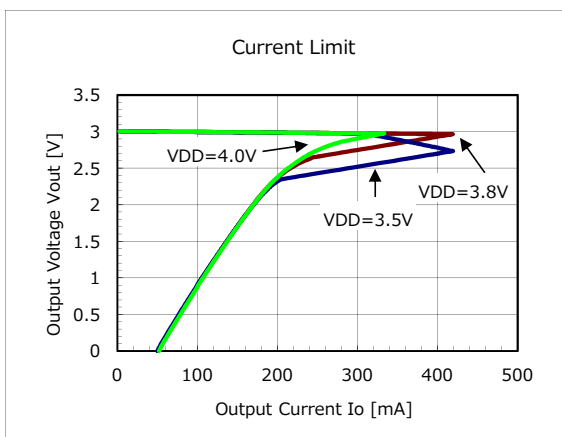
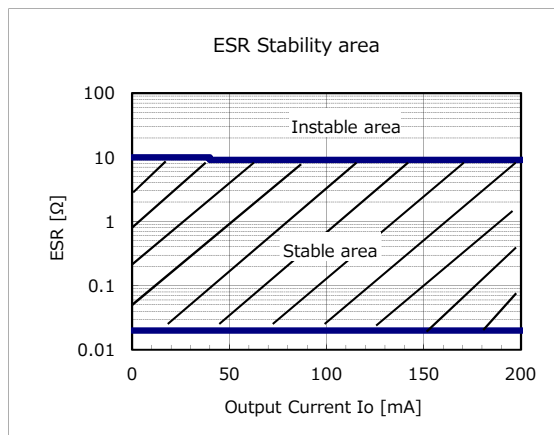
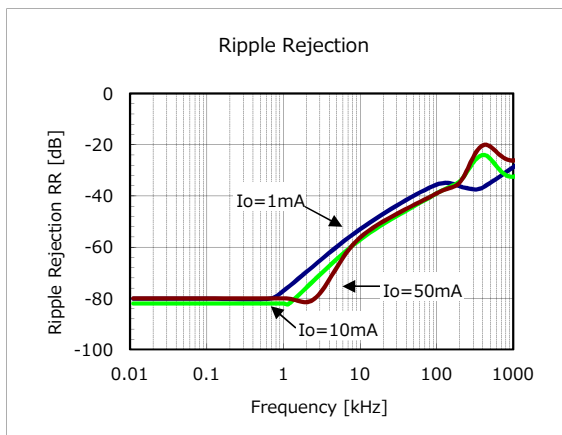
($V_{DD}=V_{OUT(Typ.)}+1V$, $V_{CE}=V_{DD}$, $T_a=25^{\circ}C$ unless otherwise specified)





Typical Performance Characteristics (3.0V)

($V_{DD}=V_{OUT}(Typ.)+1V$, $V_{CE}=V_{DD}$, $T_a=25^{\circ}C$ unless otherwise specified)





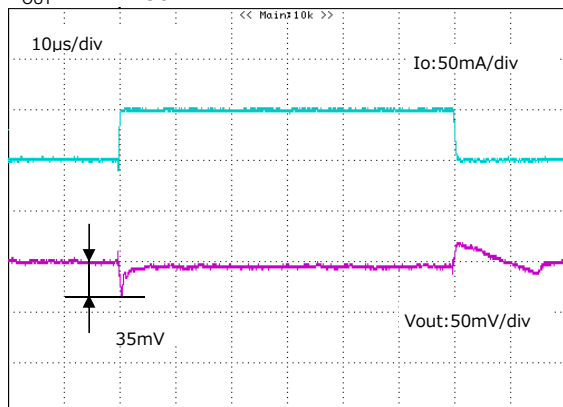
Typical Performance Characteristics (3.0V)

($V_{DD}=V_{OUT}(Typ.)+1V$, $V_{CE}=V_{DD}$, $T_a=25^\circ C$ unless otherwise specified)

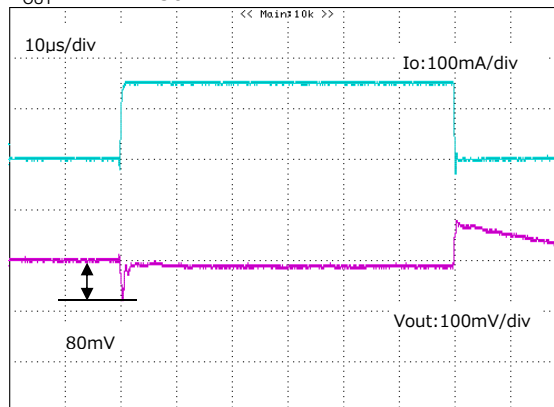
Load transient response

($V_{DD}=V_{OUT}+1V$, $V_{CE}=V_{DD}$, $C_{in}=C_o=0.47\mu F$)

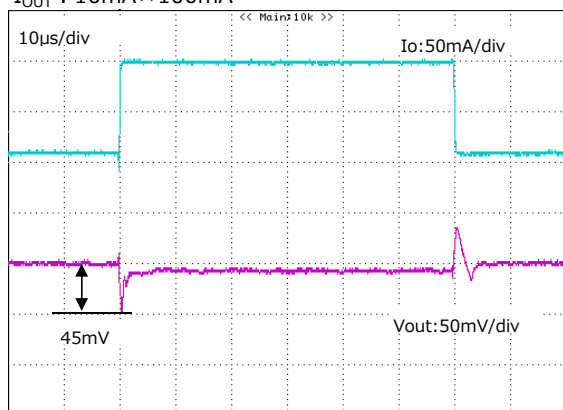
$I_{OUT} : 1mA \leftrightarrow 50mA$



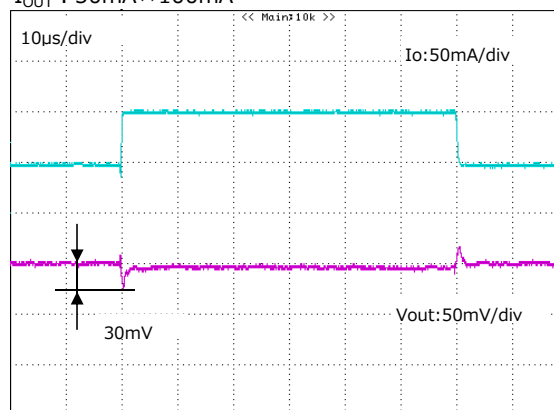
$I_{OUT} : 1mA \leftrightarrow 150mA$



$I_{OUT} : 10mA \leftrightarrow 100mA$

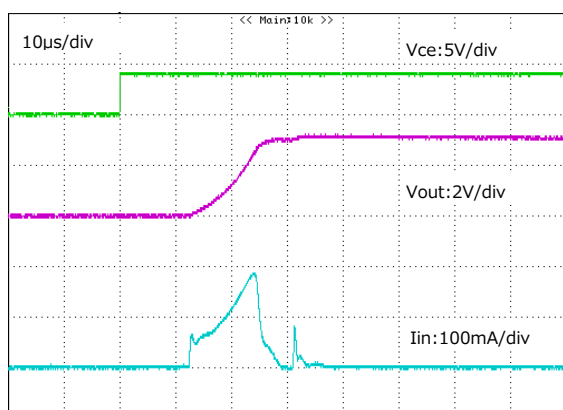


$I_{OUT} : 50mA \leftrightarrow 100mA$

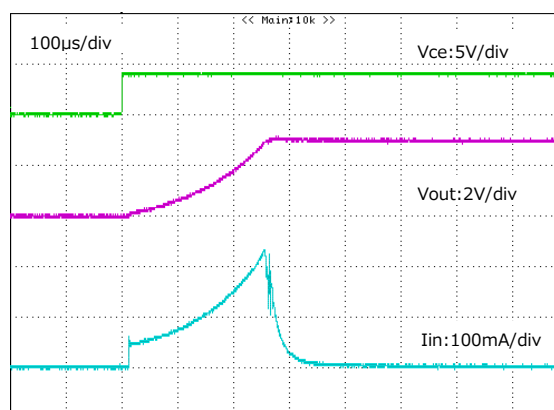


CE transient

($V_{DD}=4.0V$, $V_{CE}=0V \rightarrow V_{DD}$, $C_o=0.47\mu F$)



($V_{DD}=4.0V$, $V_{CE}=0V \rightarrow V_{DD}$, $C_o=10\mu F$)



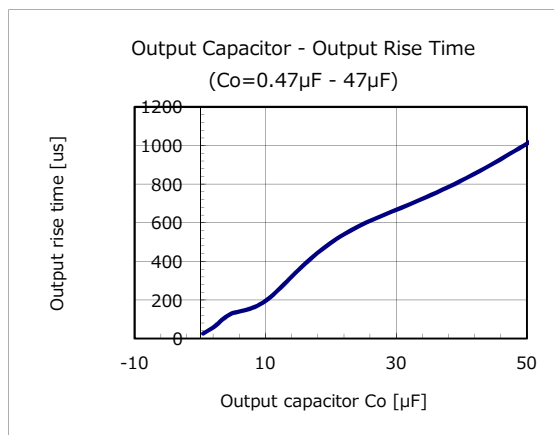
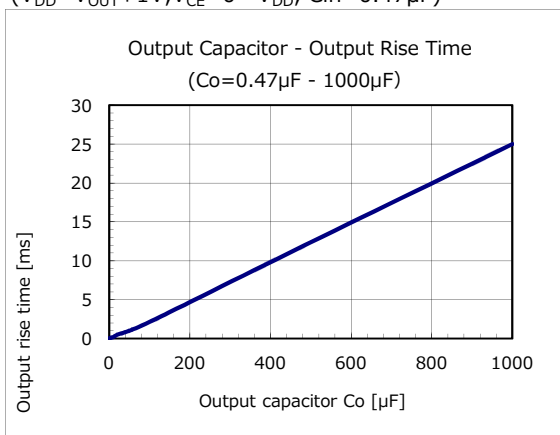


Typical Performance Characteristics (3.0V)

($V_{DD}=V_{OUT}(Typ.)+1V$, $V_{CE}=V_{DD}$, $T_a=25^{\circ}C$ unless otherwise specified)

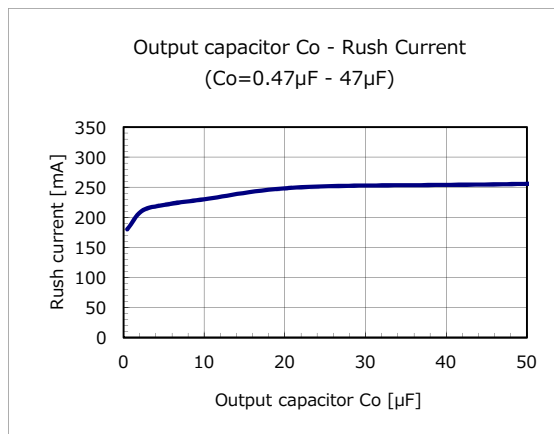
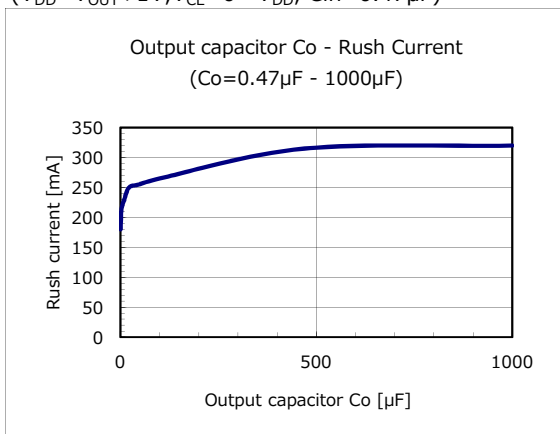
■ Output Rise Time

($V_{DD}=V_{OUT}+1V$, $V_{CE}=0 \rightarrow V_{DD}$, $C_{in}=0.47\mu F$)



■ Rush Current

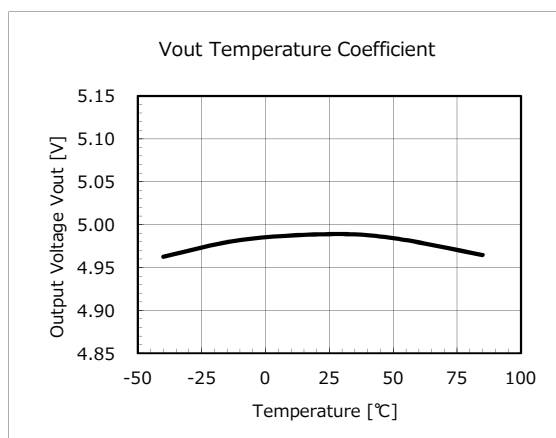
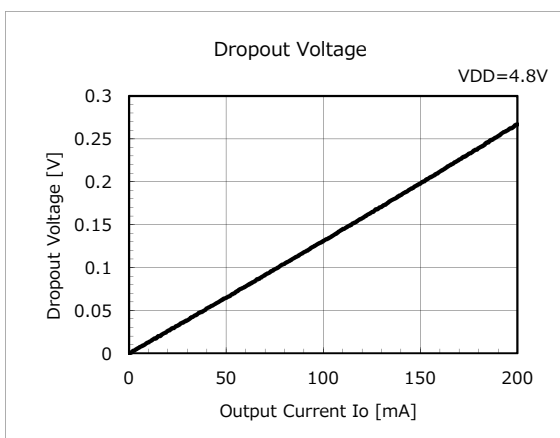
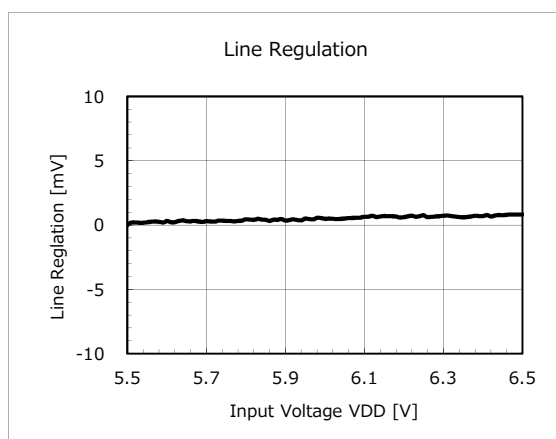
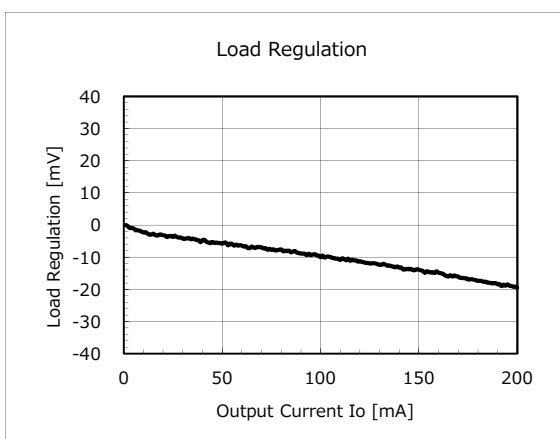
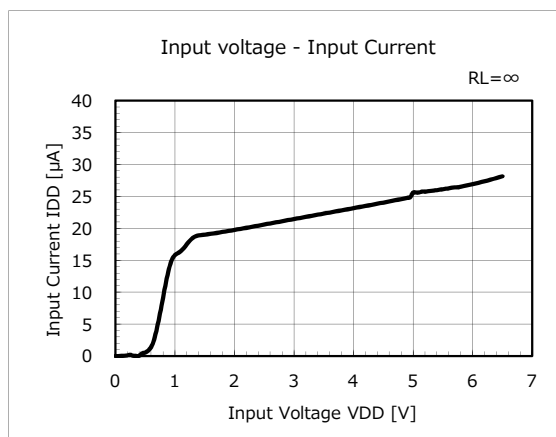
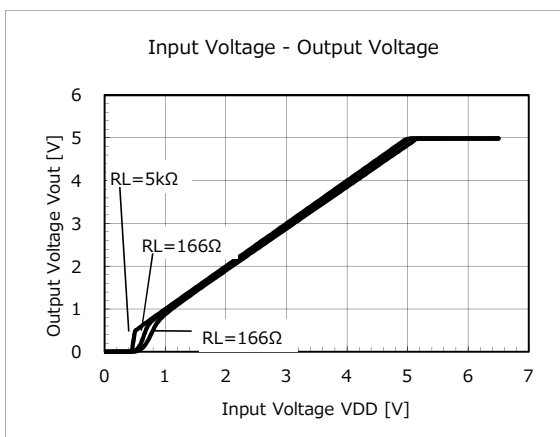
($V_{DD}=V_{OUT}+1V$, $V_{CE}=0 \rightarrow V_{DD}$, $C_{in}=0.47\mu F$)





Typical Performance Characteristics (5.0V)

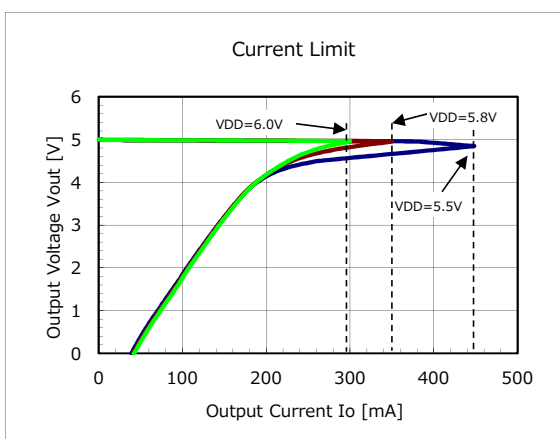
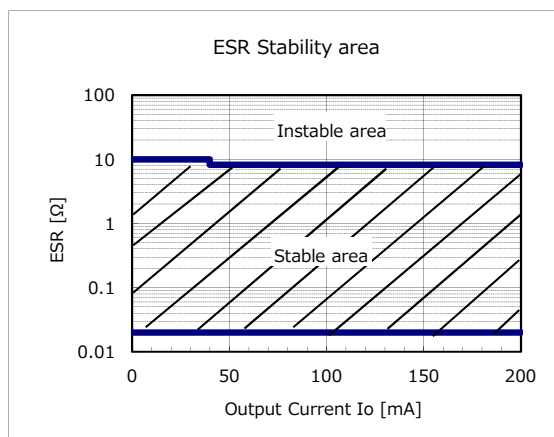
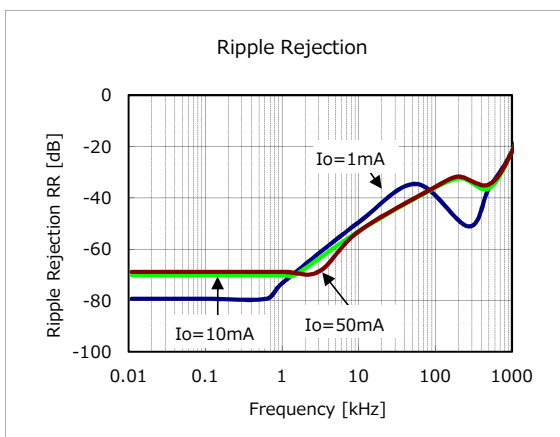
($V_{DD}=V_{OUT(Typ.)}+1V$, $V_{CE}=V_{DD}$, $T_a=25^{\circ}C$ unless otherwise specified)





Typical Performance Characteristics (5.0V)

($V_{DD}=V_{OUT(Typ.)}+1V$, $V_{CE}=V_{DD}$, $T_a=25^{\circ}C$ unless otherwise specified)





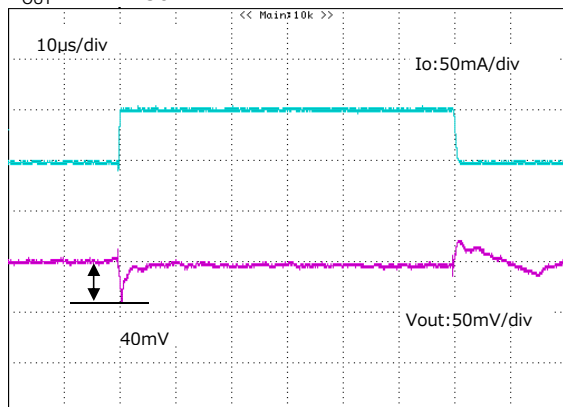
Typical Performance Characteristics (5.0V)

($V_{DD}=V_{OUT}(Typ.)+1V$, $V_{CE}=V_{DD}$, $T_a=25^\circ C$ unless otherwise specified)

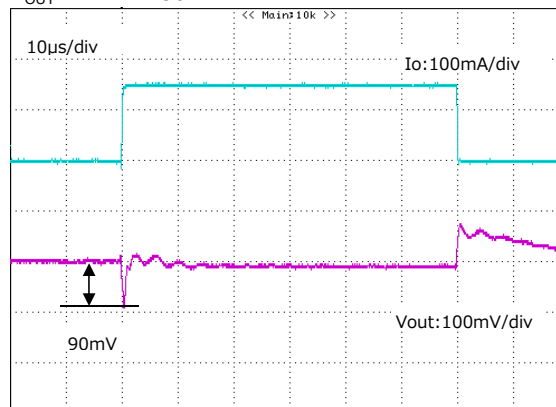
Load transient response

($V_{DD}=V_{OUT}+1V$, $V_{CE}=V_{DD}$, $C_{in}=C_o=0.47\mu F$)

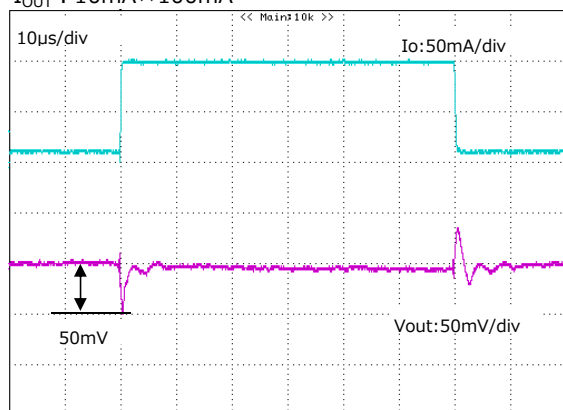
$I_{OUT} : 1mA \leftrightarrow 50mA$



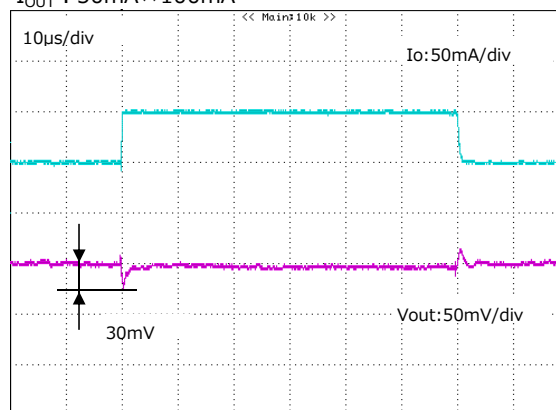
$I_{OUT} : 1mA \leftrightarrow 150mA$



$I_{OUT} : 10mA \leftrightarrow 100mA$

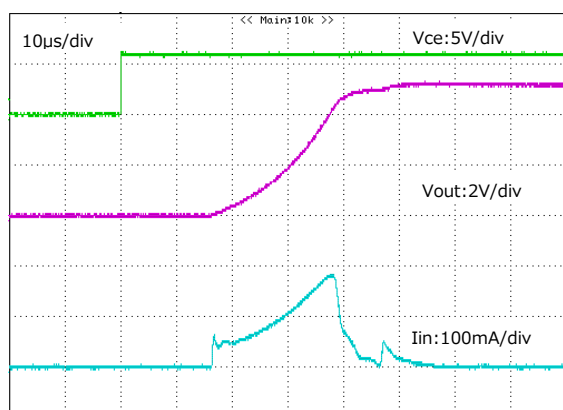


$I_{OUT} : 50mA \leftrightarrow 100mA$

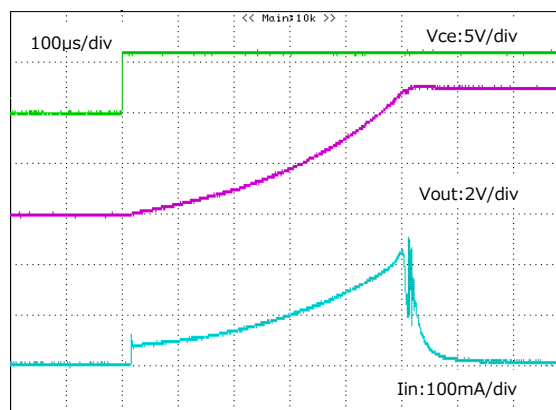


CE transient

($V_{DD}=6.0V$, $V_{CE}=0V \rightarrow V_{DD}$, $C_o=0.47\mu F$)



($V_{DD}=6.0V$, $V_{CE}=0V \rightarrow V_{DD}$, $C_o=10\mu F$)



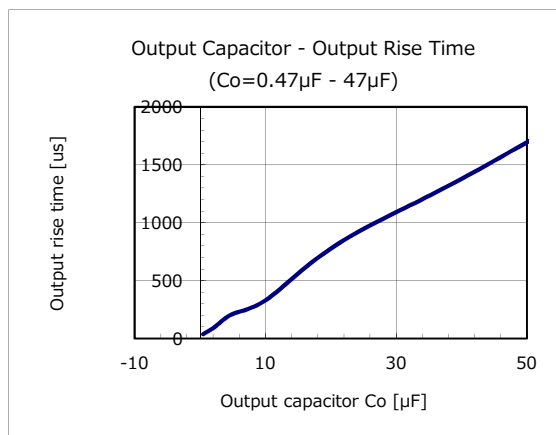
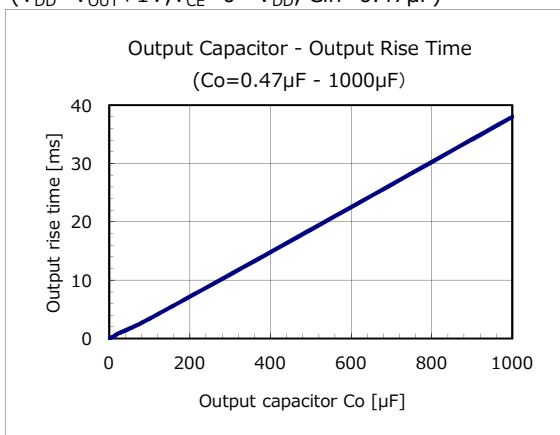


Typical Performance Characteristics (5.0V)

$(V_{DD}=V_{OUT(Typ.)}+1V, V_{CE}=V_{DD}, T_a=25^{\circ}C \text{ unless otherwise specified})$

Output Rise Time

$(V_{DD}=V_{OUT}+1V, V_{CE}=0 \rightarrow V_{DD}, C_{in}=0.47\mu F)$



Rush Current

$(V_{DD}=V_{OUT}+1V, V_{CE}=0 \rightarrow V_{DD}, C_{in}=0.47\mu F)$

